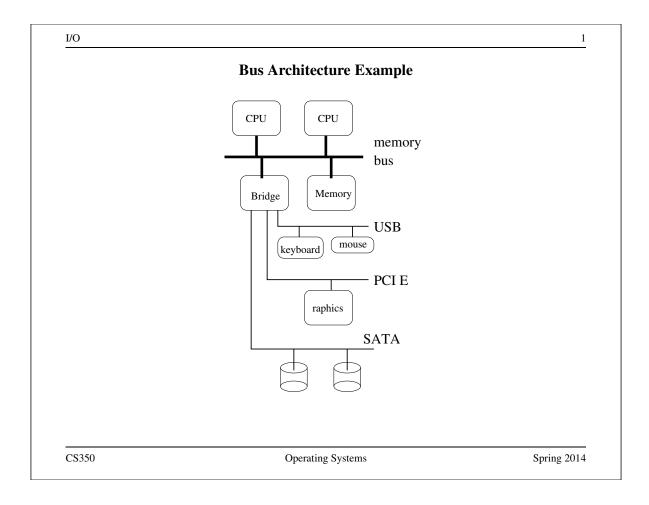
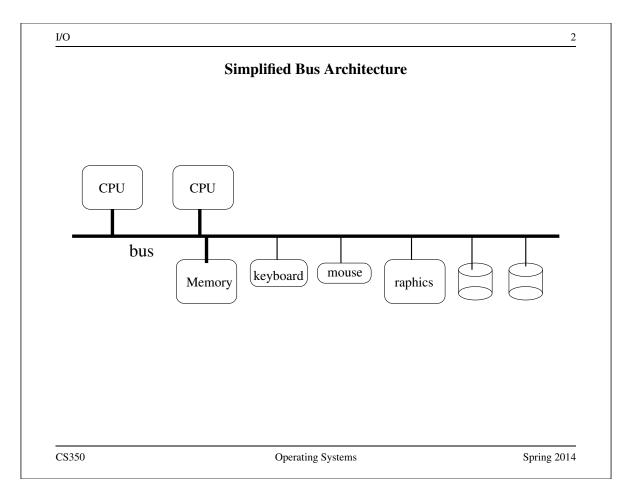
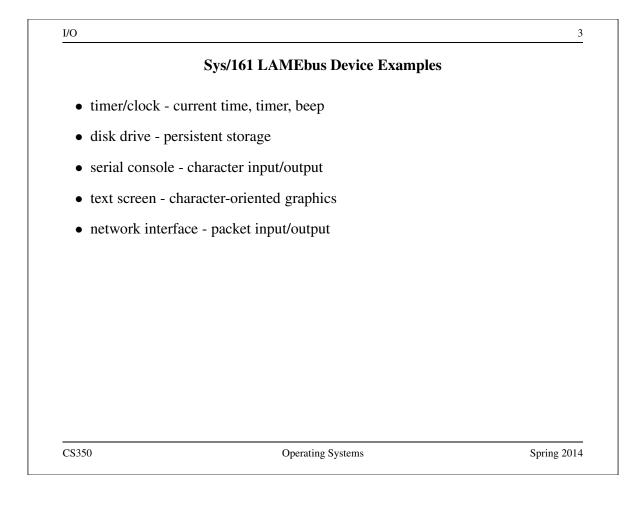
CS 350 Operating Systems Course Notes (Part 2)

Spring 2014

David R. Cheriton School of Computer Science University of Waterloo







	Dev	vice Register Example	: Sys/161 timer/clock
Offset	Size	Туре	Description
0	4	status	current time (seconds)
4	4	status	current time (nanoseconds)
8	4	command	restart-on-expiry
12	4	status and command	interrupt (reading clears)
16	4	status and command	countdown time (microseconds)
20	4	command	speaker (causes beeps)

Device Register Example: Sys/161 disk controller

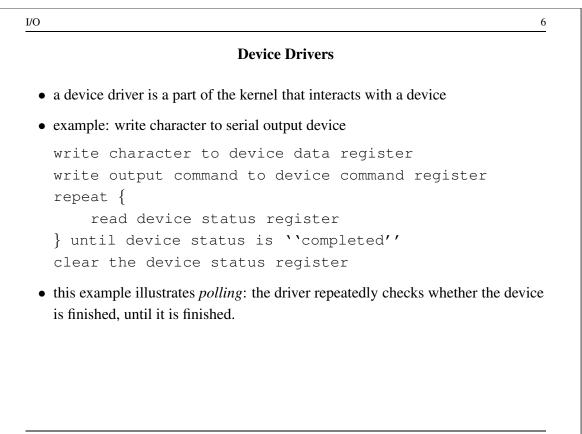
Offset	Size	Туре	Description
0	4	status	number of sectors
4	4	status and command	status
8	4	command	sector number
12	4	status	rotational speed (RPM)
32768	512	data	transfer buffer

CS350

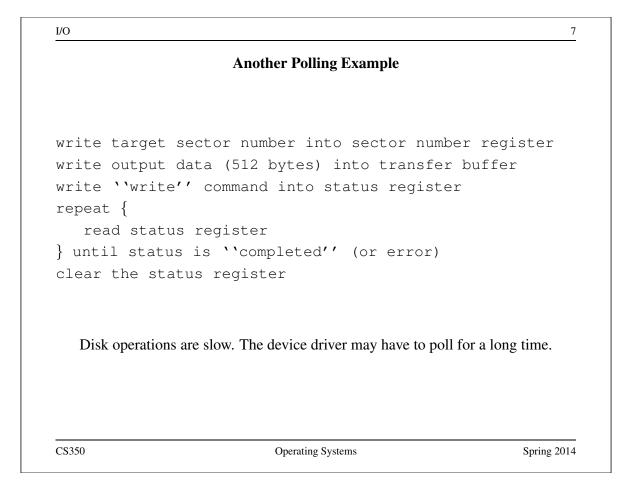
Operating Systems

Spring 2014

5



CS350



	Using Interrupts to Avoid Polling
 pollin finish 	ng can be avoided if the device can use interrupts to indicate that it is ed
exam	ple: disk write operation using interrupts:
writ writ bloc reac	te target sector number into sector number register de output data (512 bytes) into transfer buffer de ''write'' command into status register ek until device generates completion interrupt d status register to check for errors ar status register
	thread running the driver is blocked, the CPU is free to run other threads l synchronization primitives (e.g., semaphores) can be used to implement

Device Data Transfer

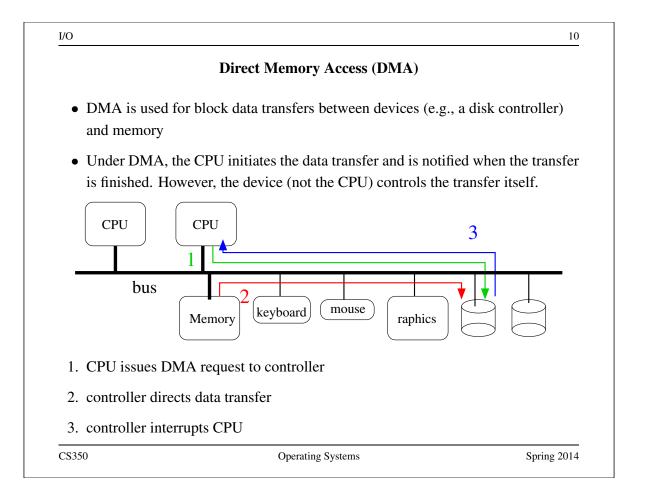
- Sometimes, a device operation will involve a large chunk of data much larger than can be moved with a single instruction.
 - example: disk read or write operation
- Devices may have data buffers for such data but how to get the data between the device and memory?
 - Option 1: program-controlled I/O
 - The device driver moves the data iteratively, one word at a time.
 - * Simple, but the CPU is *busy* while the data is being transferred.
 - Option 2: direct memory access (DMA)
 - * CPU is not busy during data transfer, and is free to do something else.

Sys/161 LAMEbus devices do program-controlled I/O.

CS350

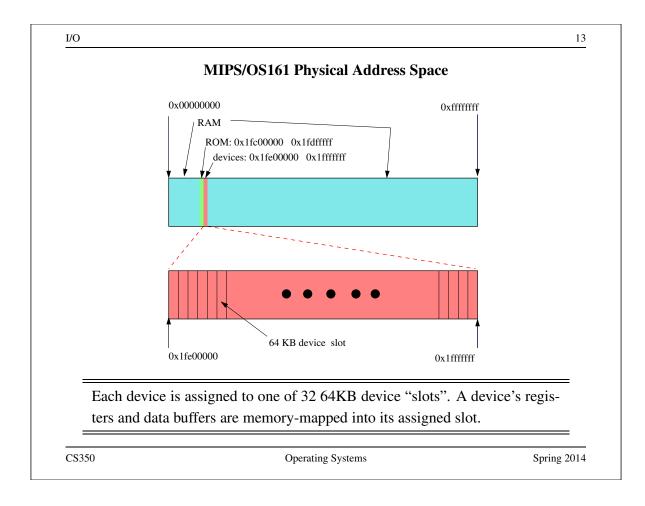
Operating Systems

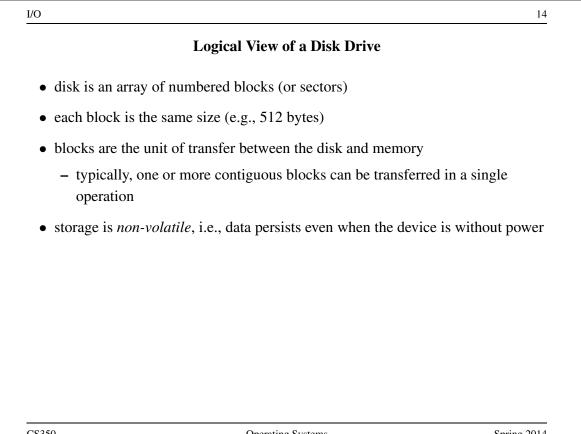
Spring 2014

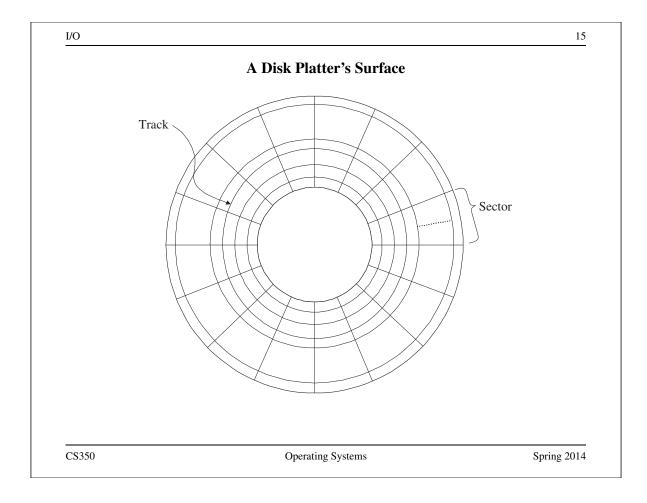


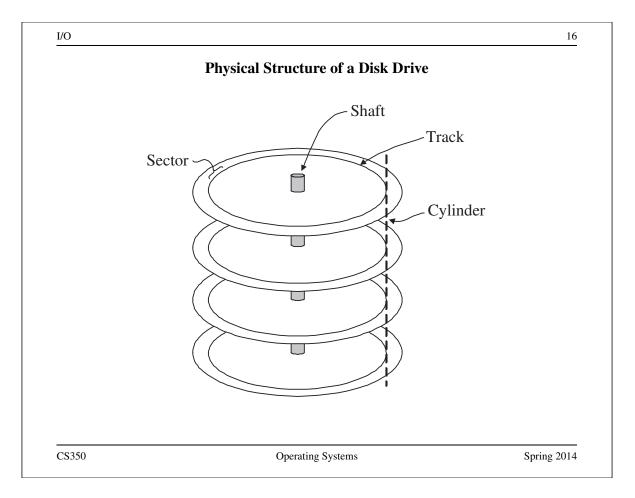
	Device Driver for Disk Write with DMA	
write targ	et disk sector number into sector numbe	er regist
write sour	ce memory address into address register	
write ''wr	ite'' command into status register	
block (sle	ep) until device generates completion i	nterrupt
read statu	s register to check for errors	
clear stat	us register	
No	te: driver no longer copies data into device transfer buffer	
C\$350	Operating Systems	Spring 2014

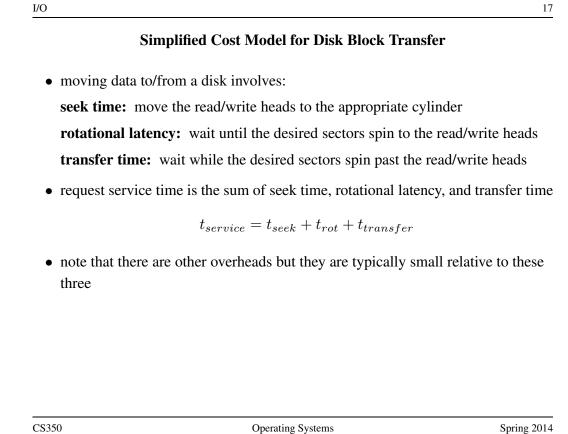
	Accessing Devices
• how can a device drive	er access device registers?
• Option 1: special I/O i	nstructions
– such as in and out	t instructions on x86
 device registers are 	e assigned "port" numbers
 instructions transfe 	r data between a specified port and a CPU register
-	
• Option 2: memory-ma	pped I/O
 each device registe 	r has a physical memory address
	read from or write to device registers using normal load ons, as though accessing memory



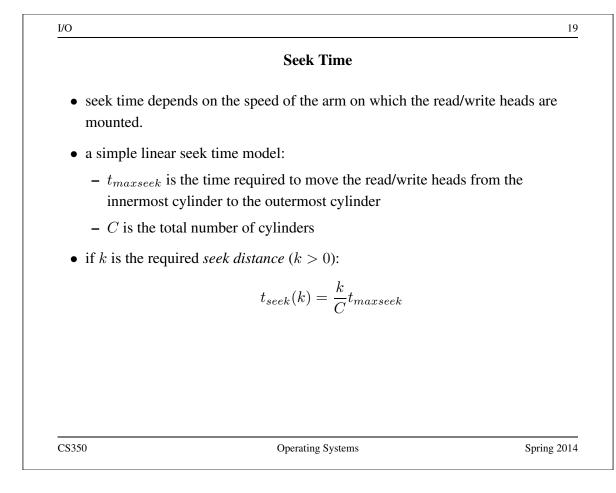


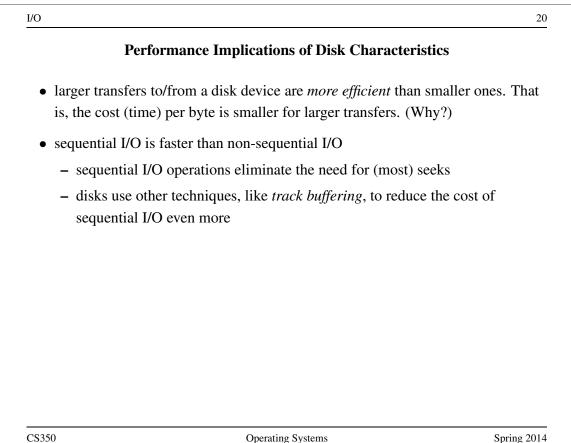






/0	1
Rotational Latency and Transfer Time	
• rotational latency depends on the rotational speed of the disk	
• if the disk spins at ω rotations per second:	
$0 \le t_{rot} \le \frac{1}{\omega}$	
• expected rotational latency:	
$\bar{t}_{rot} = \frac{1}{2\omega}$	
• transfer time depends on the rotational speed and on the amount transferred	of data
• if k sectors are to be transferred and there are T sectors per track	k:
$t_{transfer} = \frac{k}{T\omega}$	





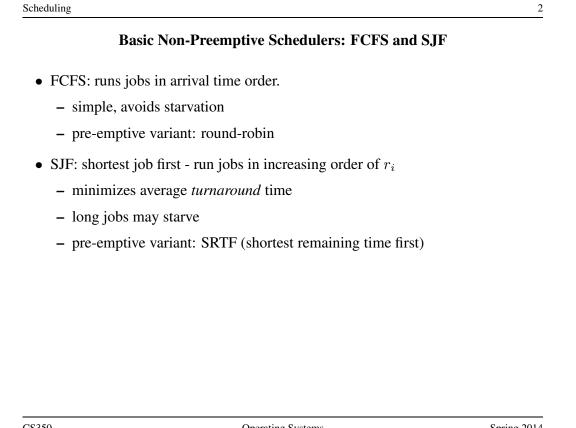
Job Scheduling Model

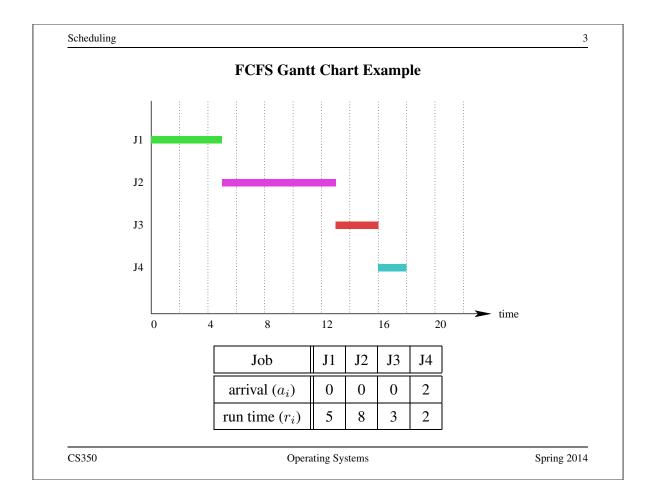
- problem scenario: a set of *jobs* needs to be executed using a single server, on which only one job at a time may run
- for the *i*th job, we have an arrival time a_i and a run time r_i
- after the *i*th job has run on the server for total time r_i , it finishes and leaves the system
- a job scheduler decides which job should be running on the server at each point in time
- let s_i ($s_i \ge a_i$) represent the time at which the *i*th job first runs, and let f_i represent the time at which the *i*th job finishes
 - the *turnaround time* of the *i*th job is $f_i a_i$
 - the response time of the *i*th job is $s_i a_i$

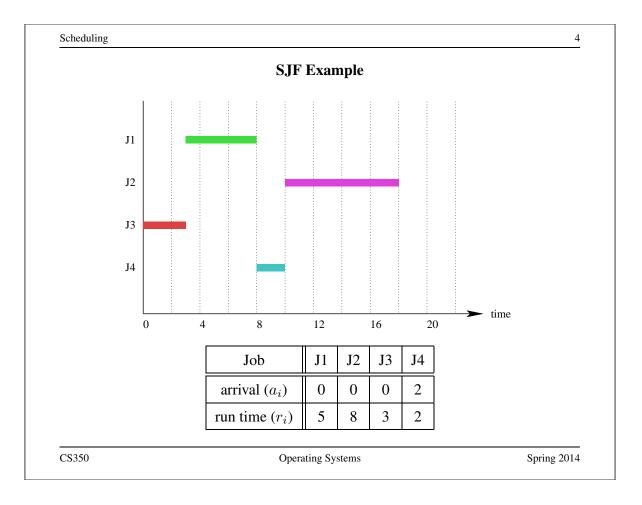
CS350

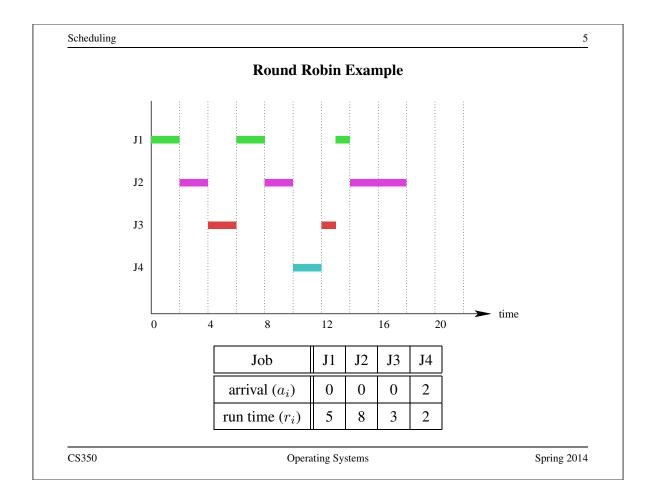
Operating Systems

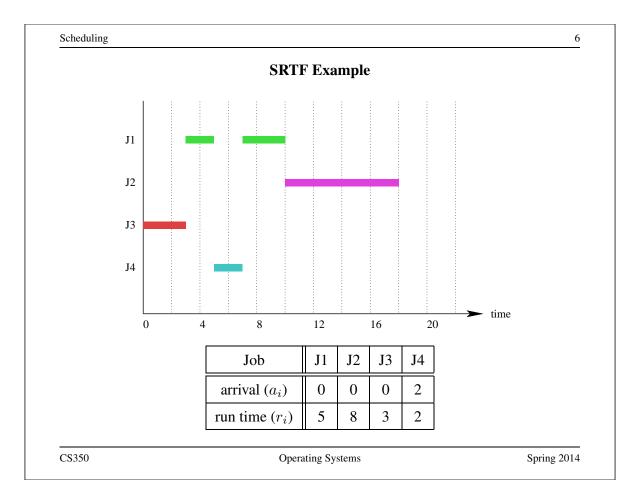
Spring 2014











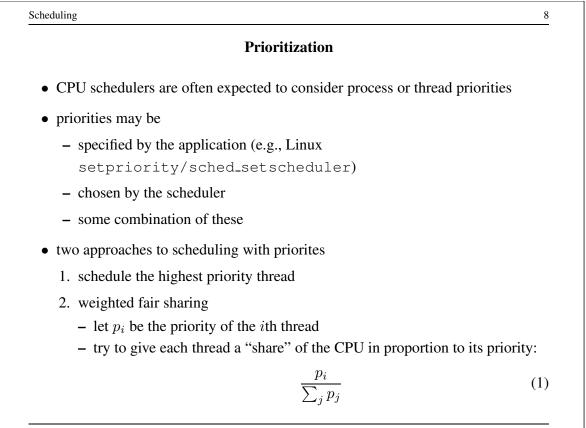
CPU Scheduling

- CPU scheduling is job scheduling where:
 - the server is a CPU (or a single core of a multi-core CPU)
 - the jobs are ready threads
 - * a thread "arrives" when it becomes ready, i.e., when it is first created, or when it wakes up from sleep
 - * the run-time of the thread is the amount of time that it will run before it either finishes or blocks
 - thread run times are typically not known in advance by the scheduler
- typical scheduler objectives
 - responsiveness low response time for some or all threads
 - "fair" sharing of the CPU
 - efficiency there is a cost to switching

CS350

Operating Systems

Spring 2014



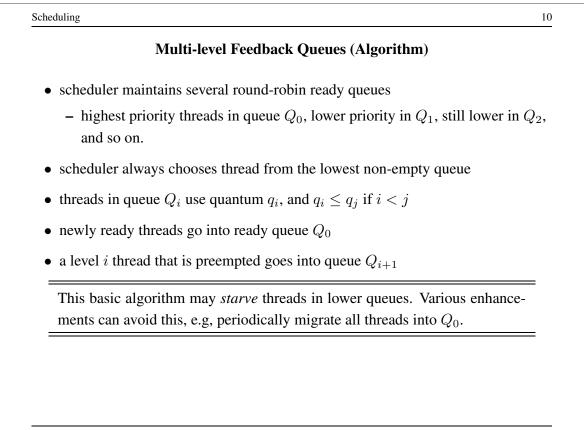
Multi-level Feedback Queues

- objective: good responsiveness for *interactive* processes
 - threads of interactive processes block frequently, have short run times
- idea: gradually diminish priority of threads with long run times and infrequent blocking
 - if a thread blocks before its quantum is used up, *raise* its priority
 - if a thread uses its entire quantum, *lower* its priority

CS350

Operating Systems

Spring 2014



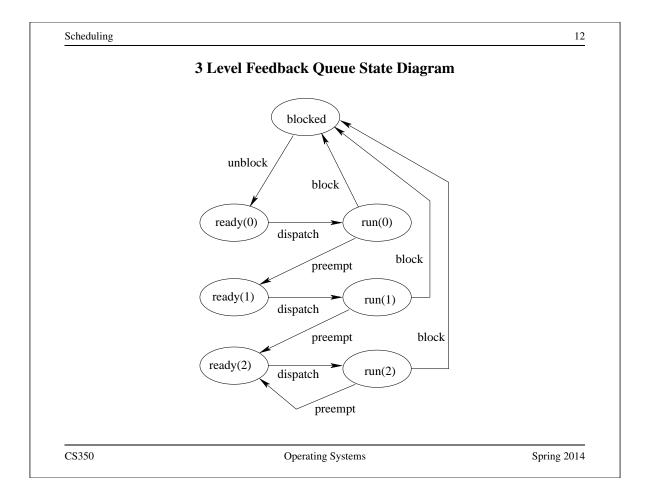
Multilevel Feedback Queues

- objective: good responsiveness for *interactive* processes
 - threads of interactive processes block frequently, have short run times
- idea: gradually diminish priority of threads with long run times and infrequent blocking
- algorithm:
 - scheduler maintains several ready queues
 - scheduler never chooses a thread in ready queue i if there are threads in any ready queue j < i.
 - threads in ready queue *i* use quantum q_i , and $q_i \leq q_j$ if i < j
 - newly ready threads go into ready queue q_0
 - a level *i* thread that is preempted goes into the level i + 1 ready queue
 - plus: some rule for raising the priority of threads in lower queues

CS350

Operating Systems

Spring 2014



Linux CFQ Scheduler - Key Ideas

- "Completely Fair Queueing" a weighted fair sharing approach
- suppose that c_i is the actual amount of time that the scheduler has allowed the *i*th thread to run.

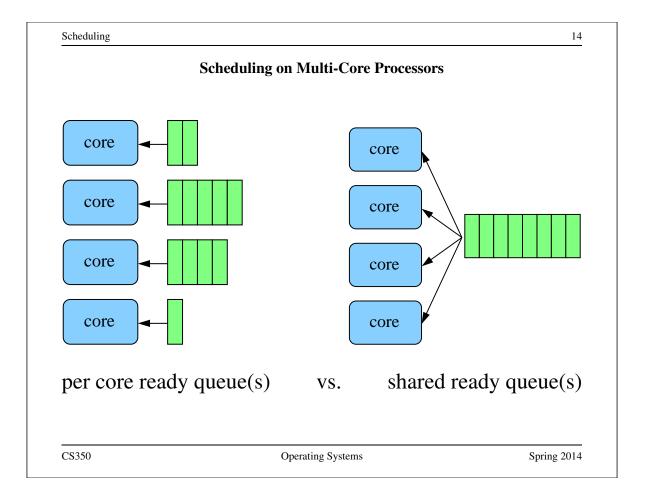
• on an *ideally shared* processor, we would expect $c_0 \frac{\sum_j p_j}{p_0} = c_1 \frac{\sum_j p_j}{p_1} = \cdots$

- CFQ calls $c_i \frac{\sum_j p_j}{p_i}$ the *virtual runtime* of the *i*th thread, and tracks it for each thread
- CFQ chooses the thread with the lowest virtual runtime, and runs it until some other thread's virtual runtime is lower (subject to a minimum runtime quantum)
 - virtual runtime advances more slowly for higher priority threads, so they get longer time slices
 - all ready threads run regularly, so good responsiveness

CS350

Operating Systems

Spring 2014



Scalability and Cache Affinity

- Contention and Scalability
 - access to shared ready queue is a critical section, mutual exclusion needed
 - as number of cores grows, contention for ready queue becomes a problem
 - per core design scales to a larger number of cores

• CPU cache affinity

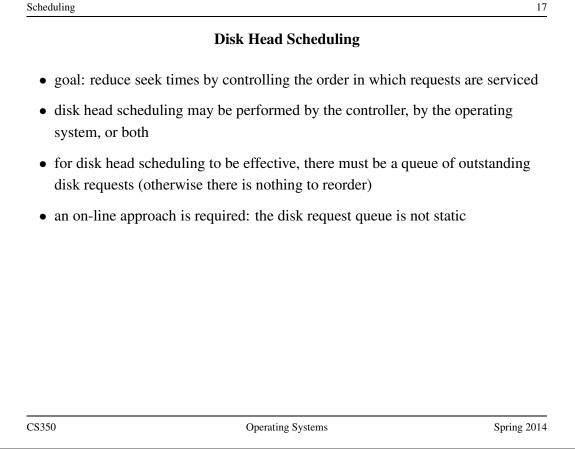
- as thread runs, data it accesses is loaded into CPU cache(s)
- moving the thread to another core means data must be reloaded into that core's caches
- as thread runs, it acquires an *affinity* for one core because of the cached data
- per core design benefits from affinity by keeping threads on the same core
- shared queue design does not

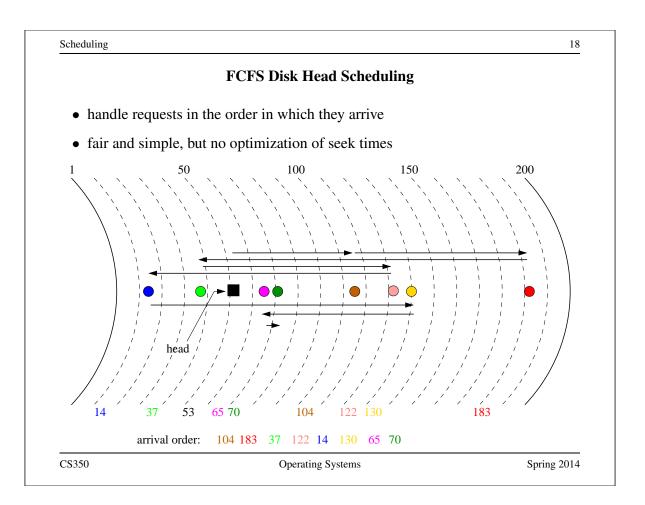
CS350

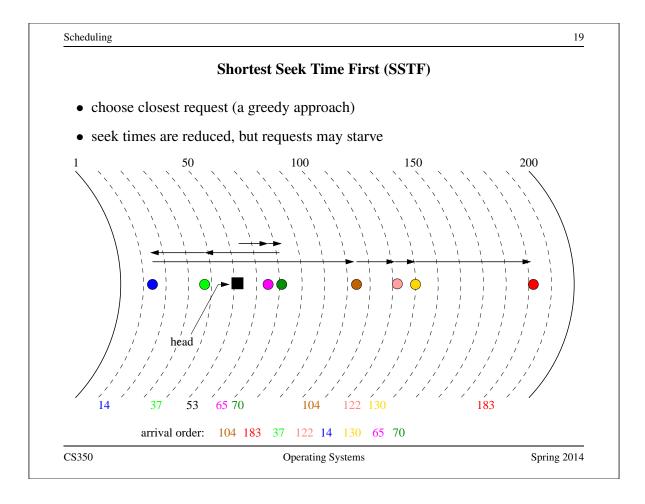
Operating Systems

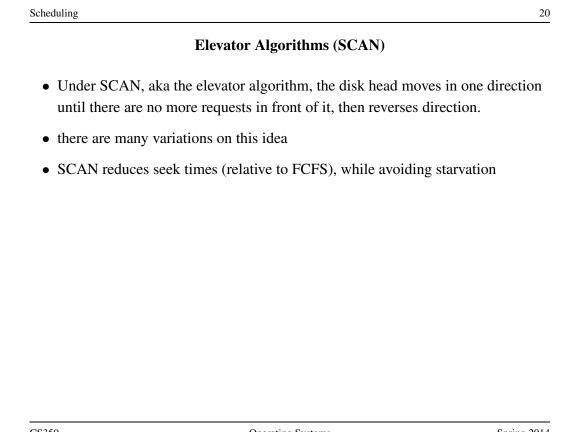
Spring 2014

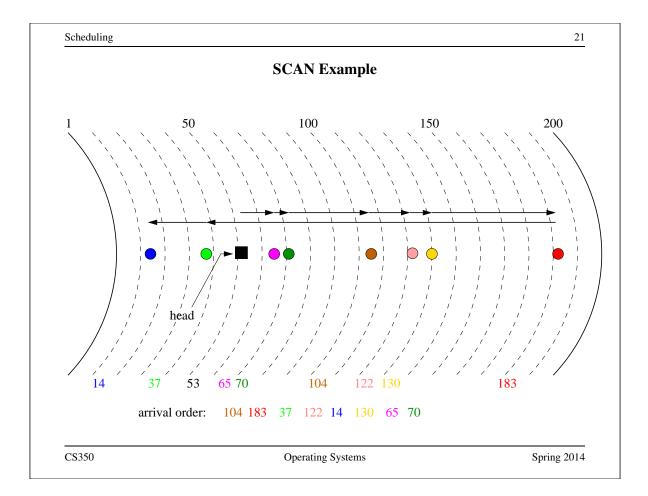
 in per-core design, queues may have different lengths this results in <i>load imbalance</i> across the cores
• this results in <i>load imbalance</i> across the cores
 cores may be idle while others are busy
 threads on lightly loaded cores get more CPU time than threads on heavily loaded cores
• not an issue in shared queue design
• per-core designs typically need some mechanism for <i>thread migration</i> to address load imbalances
 migration means moving threads from heavily loaded cores to lightly load cores

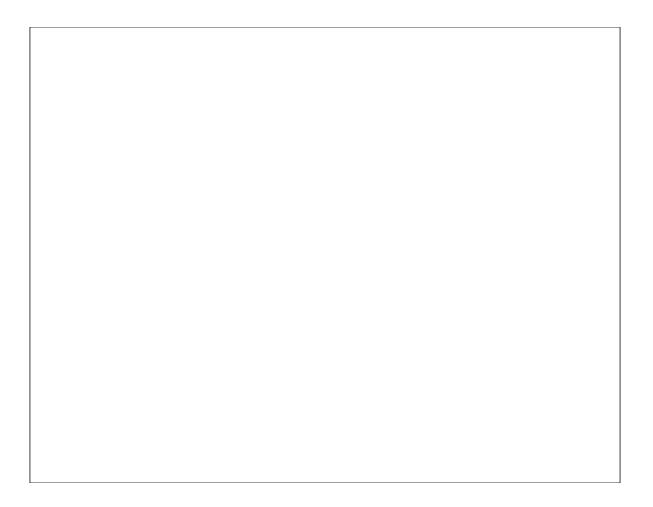


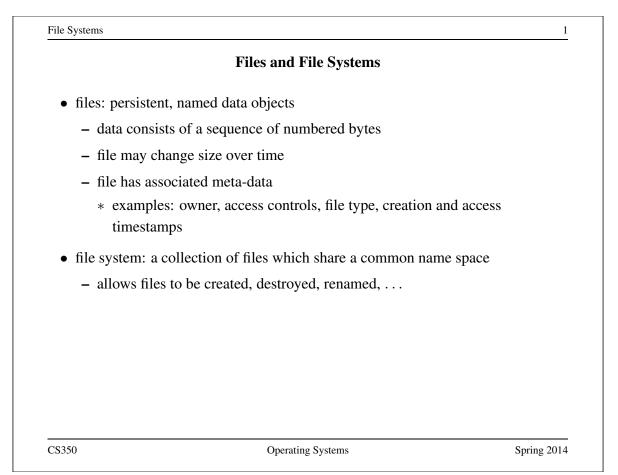




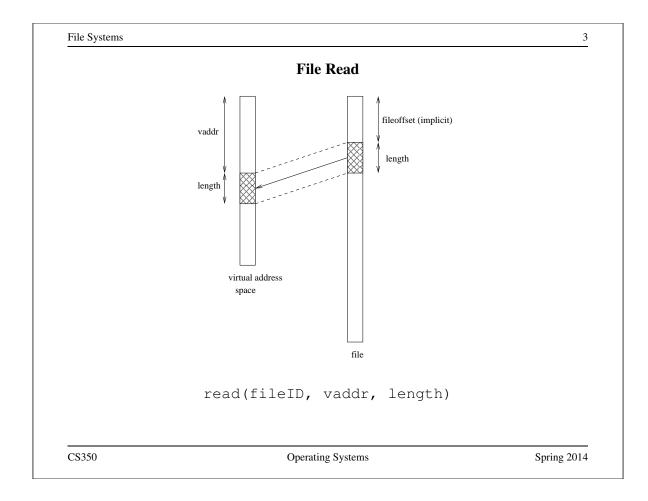


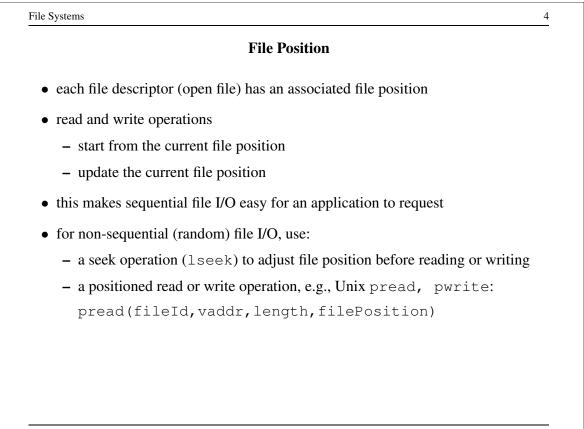






 pen, close open returns a file identifier (or handle or descriptor), which is used in subsequent operations to identify the file. (Why is this done?) ead, write, seek read copies data from a file into a virtual address space write copies data from a virtual address space into a file
subsequent operations to identify the file. (Why is this done?) ead, write, seek - read copies data from a file into a virtual address space
- read copies data from a file into a virtual address space
A A
- write copies data from a virtual address space into a file
- seek enables non-sequential reading/writing
et/set file meta-data, e.g., Unix fstat, chmod





File Systems

Sequential File Reading Example (Unix)

```
char buf[512];
int i;
int f = open("myfile",O_RDONLY);
for(i=0; i<100; i++) {
  read(f,(void *)buf,512);
}
close(f);
```

Read the first 100 * 512 bytes of a file, 512 bytes at a time.

CS350

Operating Systems

Spring 2014

File Reading Example Using Seek (Unix)	
char buf[512];	
int i;	
<pre>int f = open("myfile",O_RDONLY);</pre>	
for(i=1; i<=100; i++) {	
lseek(f,(100-i)*512,SEEK_SET);	
<pre>read(f,(void *)buf,512);</pre>	
close(f);	

File Systems

File Reading Example Using Positioned Read

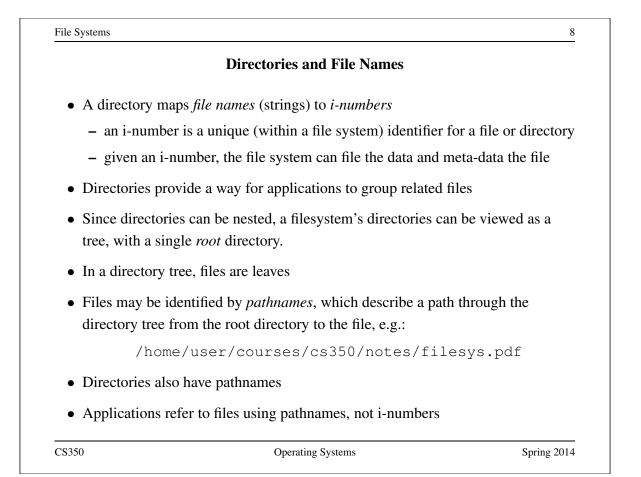
```
char buf[512];
int i;
int f = open("myfile",O_RDONLY);
for(i=0; i<100; i+=2) {
    pread(f,(void *)buf,512,i*512);
}
close(f);
```

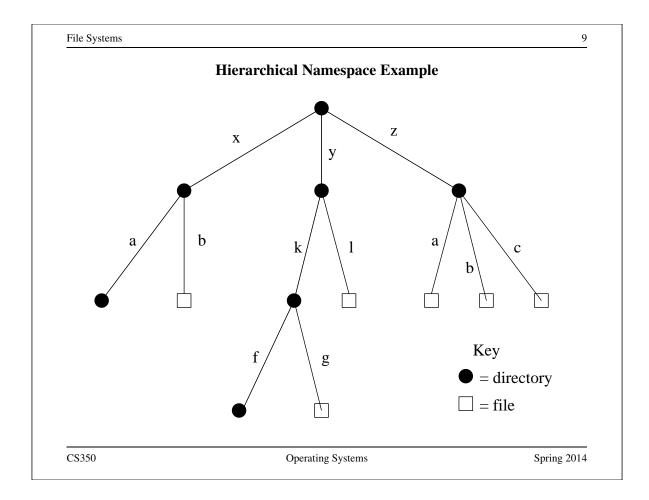
Read every second 512 byte chunk of a file, until 50 have been read.

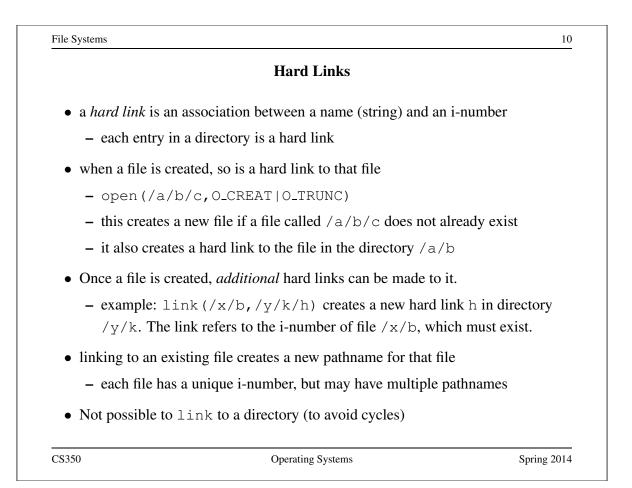
CS350

Operating Systems

Spring 2014







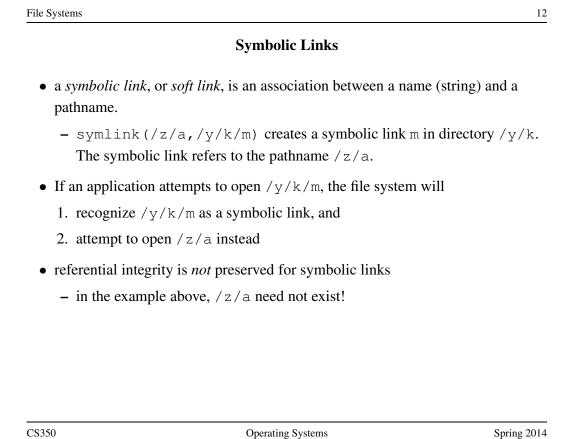
Unlinking and Referential Integrity

- hard links can be removed:
 - unlink (/x/b)
- the file system ensures that hard links have *referential integrity*, which means that if the link exists, the file that it refers to also exists.
 - When a hard link is created, it refers to an existing file.
 - There is no system call to delete a file. Instead, a file is deleted when its last hard link is removed.

CS350

Operating Systems

Spring 2014



File Systems

UNIX/Linux Link Example (1 of 3)

% cat > file1 This is file1. <cntl-d> % ls -li 685844 -rw------ 1 user group 15 2008-08-20 file1 % ln file1 link1 % ln -s file1 sym1 % ln not-here link2 ln: not-here: No such file or directory % ln -s not-here sym2

Files, hard links, and soft/symbolic links.

CS350

Operating Systems

Spring 2014

13

File Systems 14 UNIX/Linux Link Example (2 of 3) % ls -li 685844 -rw----- 2 user group 15 2008-08-20 file1 685844 -rw----- 2 user group 15 2008-08-20 link1 685845 lrwxrwxrwx 1 user group 5 2008-08-20 sym1 -> file1 685846 lrwxrwxrwx 1 user group 8 2008-08-20 sym2 -> not-here % cat file1 This is file1. % cat link1 This is file1. % cat sym1 This is file1. % cat sym2 cat: sym2: No such file or directory % /bin/rm file1 Accessing and manipulating files, hard links, and soft/symbolic links.

CS350

File Systems

UNIX/Linux Link Example (3 of 3)

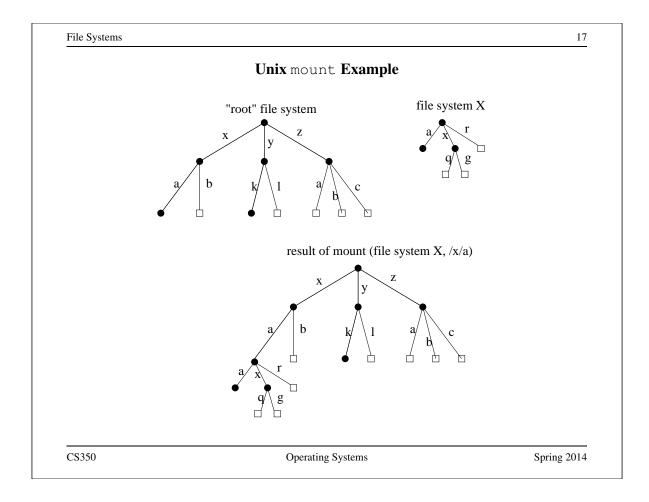
```
% ls −li
685844 -rw----- 1 user group 15 2008-08-20 link1
685845 lrwxrwxrwx 1 user group 5 2008-08-20 sym1 -> file1
685846 lrwxrwxrwx 1 user group 8 2008-08-20 sym2 -> not-here
% cat link1
This is file1.
% cat sym1
cat: sym1: No such file or directory
% cat > file1
This is a brand new file1.
<cntl-d>
% ls -li
685847 -rw----- 1 user group 27 2008-08-20 file1
685844 -rw----- 1 user group 15 2008-08-20 link1
685845 lrwxrwxrwx 1 user group 5 2008-08-20 sym1 -> file1
685846 lrwxrwxrwx 1 user group 8 2008-08-20 sym2 -> not-here
% cat link1
This is file1.
% cat sym1
This is a brand new file1.
   Different behaviour for hard links and soft/symbolic links.
```

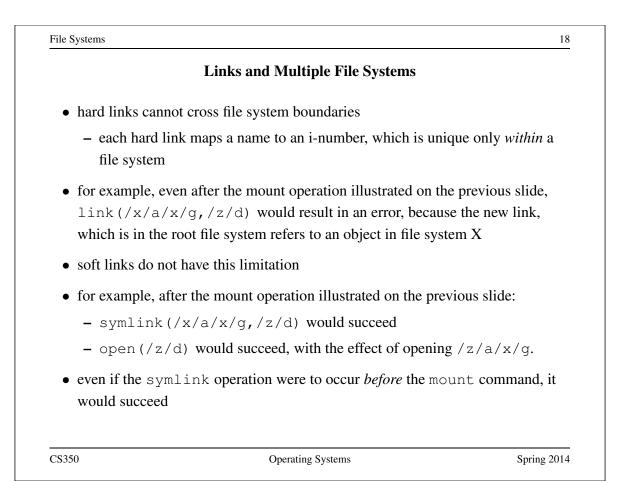
CS350

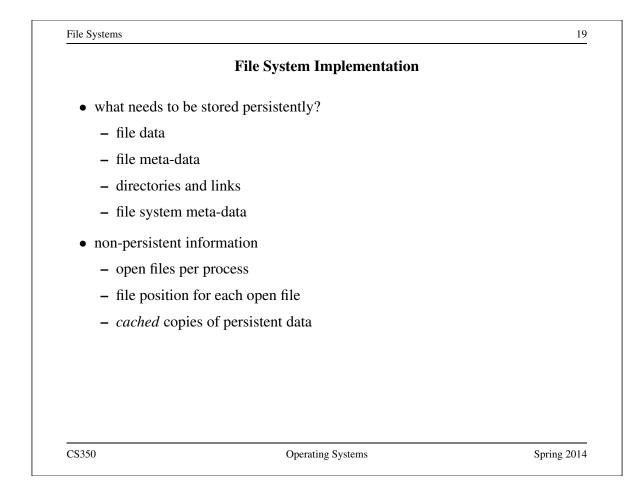
Operating Systems

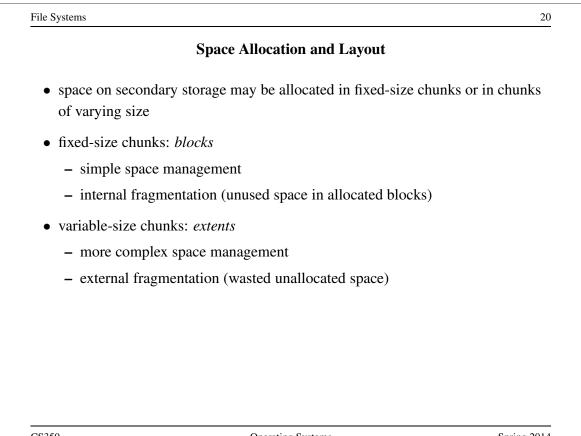
Spring 2014

ile Systems		16
	Multiple File Systems	
• it is not uncommon fo	r a system to have multiple file system	ns
• some kind of global fi	le namespace is required	
• two examples:		
DOS/Windows: use file system	two-part file names: file system name	, pathname within
- example: C:\	user\cs350\schedule.txt	
Unix: create single here two file systems	ierarchical namespace that combines t	the namespaces of
- Unix mount sy	stem call does this	
• mounting does <i>not</i> ma	ke two file systems into one file syste	em
 it merely creates a namespaces of two 	single, hierarchical namespace that co file systems	ombines the
 the new namespace unmounted 	e is temporary - it exists only until the	e file system is
\$350	Operating Systems	Spring 2014



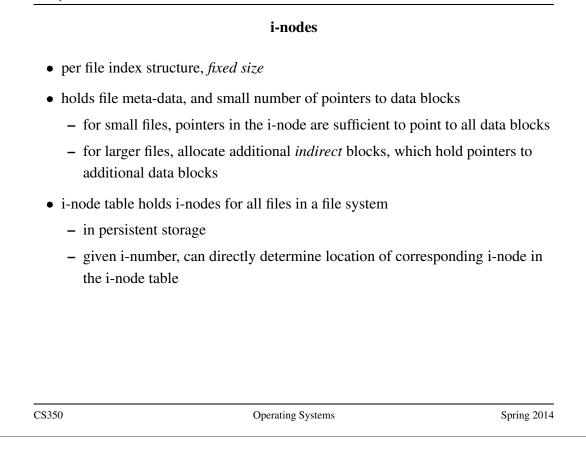




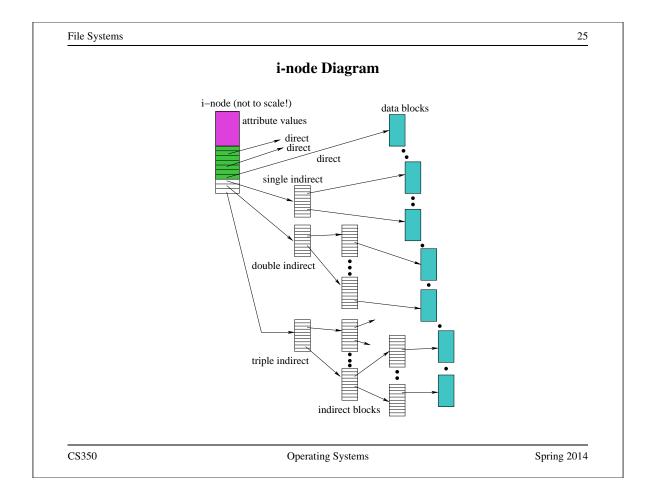


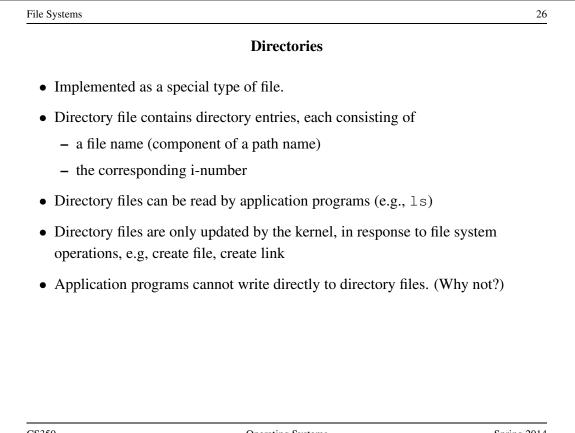
	fixed-s	size allocation			
	variabl	e-size allocatior	ı		
	1		1 01		
	ers on secondary s tial extents that ca		-	-	-
iuige sequen					

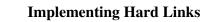
	File Indexing	
• where is the data for	r a given file?	
• common solution: p	er-file indexing	
– for each file, an i	index with pointers to data blocks or extents	
* in extent-base	d systems, need pointer and length for each exte	ent
• how big should the i	index be?	
- need to accomme	odate both small files and very large files	
- approach: allow	different index sizes for different files	
\$350	Operating Systems	Spring 201



File Systems		
	Example: Linux ext3 i-nodes	
• i-node fields		
– file type		
– file permissions		
– file length		
– number of file bl	ocks	
– time of last file a	ccess	
– time of last i-nod	le update, last file update	
– number of hard l	inks to this file	
– 12 <i>direct</i> data blo	ock pointers	
– one single, one d	ouble, one triple indirect data block poin	nter
• i-node size: 128 byte	es	
• i-node table: broken secondary storage de	into smaller tables, each in a known loca evice (disk)	ation on the
CS350	Operating Systems	Spring 2014







- hard links are simply directory entries
- for example, consider:

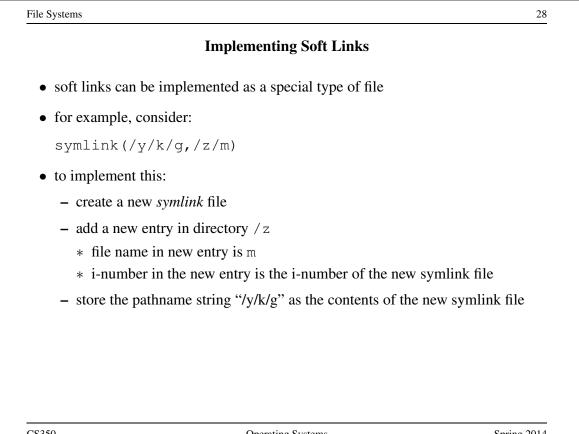
link(/y/k/g,/z/m)

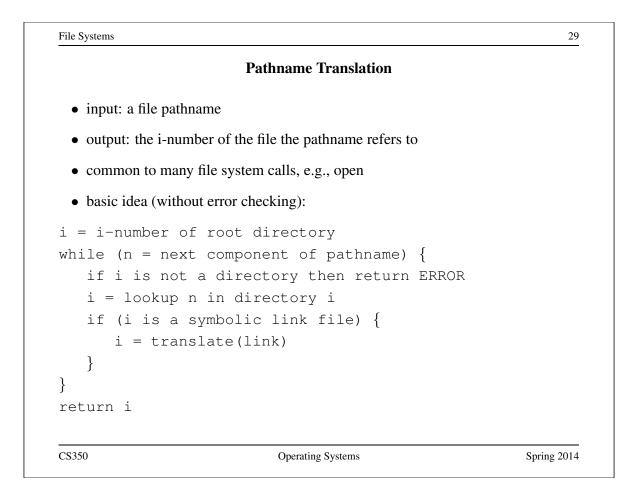
- to implement this:
 - 1. find out the internal file identifier for /y/k/g
 - 2. create a new entry in directory /z
 - file name in new entry is m
 - file identifier (i-number) in the new entry is the one discovered in step 1

CS350

Operating Systems

Spring 2014



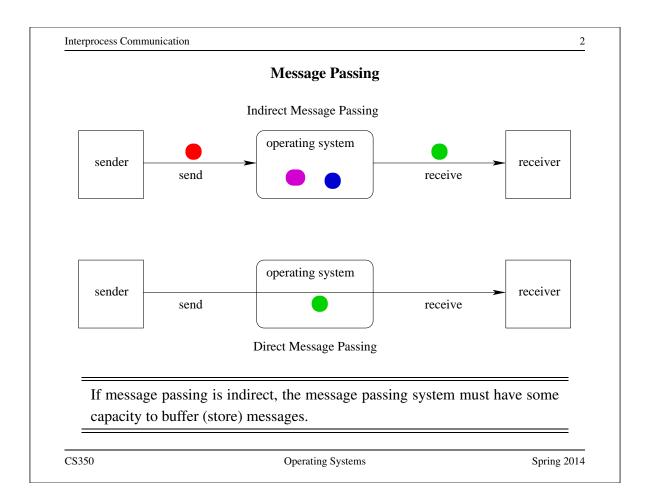


ile Systems		30
In	-Memory (Non-Persistent) Structures	
• per process		
 descriptor table 		
* which file de	scriptors does this process have open?	
* to which file	does each open descriptor refer?	
* what is the cu	arrent file position for each descriptor?	
• system wide		
– open file table		
* which files a	re currently open (by any process)?	
– i-node cache		
* in-memory co	opies of recently-used i-nodes	
 block cache 		
* in-memory c	opies of data blocks and indirect blocks	
2\$350	Operating Systems	Spring 2014

Fire system of the system operation may require several disk I/O operations e a single logical file system operation may require several disk I/O operations e example: deleting a file remove entry from directory remove file index (i-node) from i-node table mark file's data blocks free in free space index what if, because of a failure, some but not all of these changes are reflected on the disk? system failure will destroy in-memory file system structures persistent structures should be *crash consistent*, i.e., should be consistent when system restarts after a failure

e consistency checkers (e.g., Unix fsck in B crash, before normal operations resume empt to repair inconsistent file system data str no directory entry e that is not marked as free ., Veritas, NTFS, Linux ext3)	
empt to repair inconsistent file system data str no directory entry e that is not marked as free	ructures, e.g.:
no directory entry e that is not marked as free	ructures, e.g.:
. Veritas, NTFS, Linux ext3)	
, · · · · · · · · · · · · · · · · · · ·	
system meta-data changes in a journal (log), s n be written to disk in a single operation	o that sequences of
es have been journaled, update the disk data s d logging)	structures
re, redo journaled updates in case they were r	not done before the
	a be written to disk in a single operation es have been journaled, update the disk data s d logging)

		-
Inter	process Communication Mechanisn	ns
• shared storage		
- shared virtual me	mory	
 shared files 		
• message-based		
– signals		
- sockets		
– pipes		



Interprocess Communication

Properties of Message Passing Mechanisms

Directionality:

- simplex (one-way), duplex (two-way)
- half-duplex (two-way, but only one way at a time)

Message Boundaries:

datagram model: message boundaries

stream model: no boundaries

Connections: need to connect before communicating?

- in connection-oriented models, recipient is specified at time of connection, not by individual send operations. All messages sent over a connection have the same recipient.
- in connectionless models, recipient is specified as a parameter to each send operation.

Reliability:

• can messages get lost? reordered? damaged?

CS350

Operating Systems

Spring 2014

	Sockets
• ;	a socket is a communication <i>end-point</i>
• i	if two processes are to communicate, each process must create its own socket
• 1	two common types of sockets
\$	stream sockets: support connection-oriented, reliable, duplex communication under the stream model (no message boundaries)
(datagram sockets: support connectionless, best-effort (unreliable), duplex communication under the datagram model (message boundaries)
• 1	both types of sockets also support a variety of address domains, e.g.,
ا	Unix domain: useful for communication between processes running on the same machine
]	INET domain: useful for communication between process running on different machines that can communicate using IP protocols.

Interprocess Communication

Using Datagram Sockets (Receiver)

s = socket(addressType, SOCK_DGRAM); bind(s,address); recvfrom(s,buf,bufLength,sourceAddress); ... close(s);

- socket creates a socket
- bind assigns an address to the socket
- recvfrom receives a message from the socket
 - buf is a buffer to hold the incoming message
 - sourceAddress is a buffer to hold the address of the message sender
- both buf and sourceAddress are filled by the recvfrom call

CS350

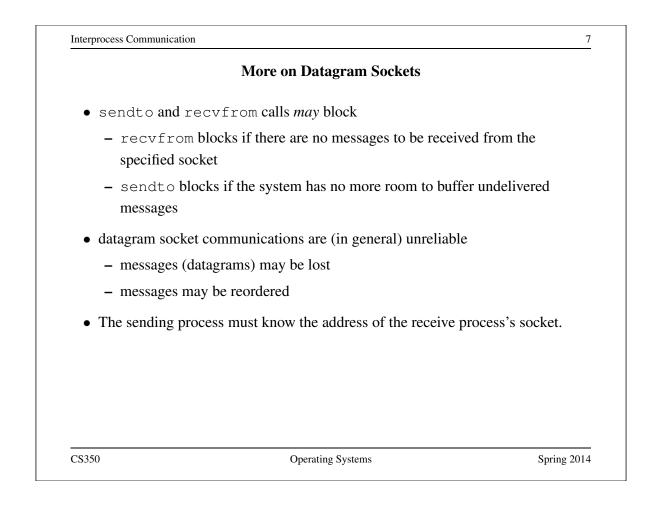
Operating Systems

Spring 2014

5

```
j>temprocessCommunication (addressStype, SOCK_DGRAM);
s = socket(addressType, SOCK_DGRAM);
sendto(s,buf,msgLength,targetAddress)
...
close(s);
• socket creates a socket
• sendto sends a message using the socket
• buf is a buffer that contains the message to be sent
• msgLength indicates the length of the message in the buffer
• targetAddress is the address of the socket to which the message is to
be delivered
```

CS350



```
Interprocess Communication
                                                                       8
                  Using Stream Sockets (Passive Process)
s = socket(addressType, SOCK_STREAM);
bind(s,address);
listen(s,backlog);
ns = accept(s, sourceAddress);
recv(ns,buf,bufLength);
send(ns,buf,bufLength);
. . .
close(ns); // close accepted connection
close(s); // don't accept more connections
 • listen specifies the number of connection requests for this socket that will be
   queued by the kernel
 • accept accepts a connection request and creates a new socket (ns)
 • recv receives up to bufLength bytes of data from the connection
 • send sends bufLength bytes of data over the connection.
CS350
                                                                 Spring 2014
                              Operating Systems
```

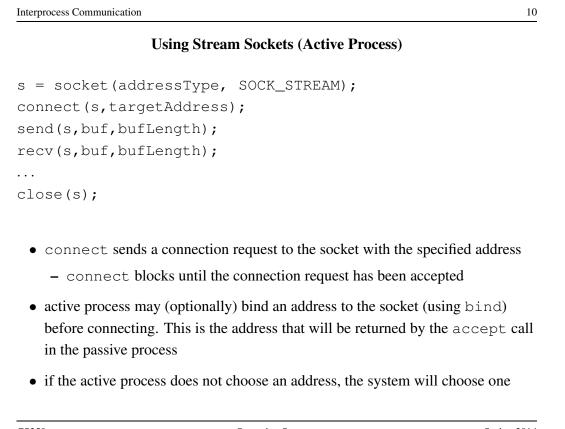
Notes on Using Stream Sockets (Passive Process)

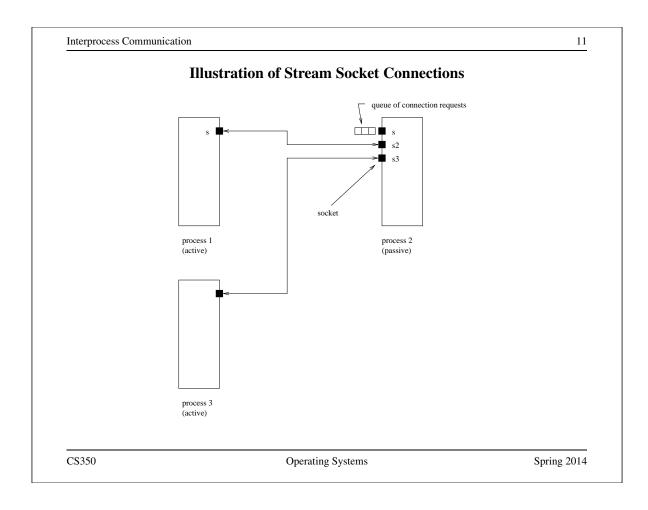
- accept creates a new socket (ns) for the new connection
- sourceAddress is an address buffer. accept fills it with the address of the socket that has made the connection request
- additional connection requests can be accepted using more accept calls on the original socket (s)
- accept blocks if there are no pending connection requests
- connection is duplex (both send and recv can be used)

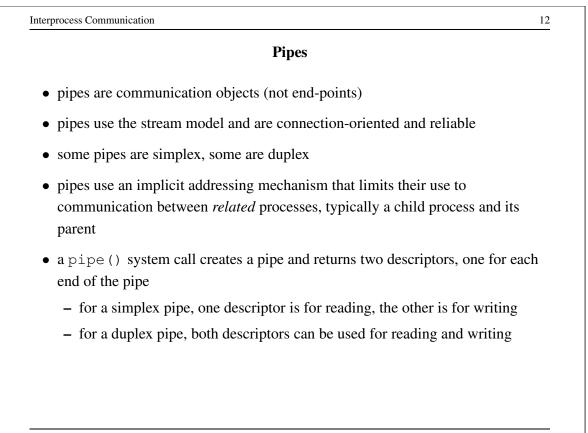
CS350

Operating Systems

Spring 2014

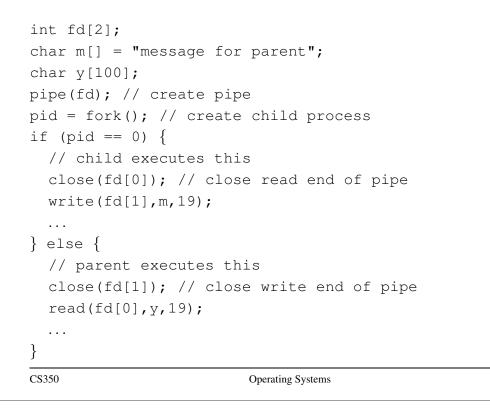


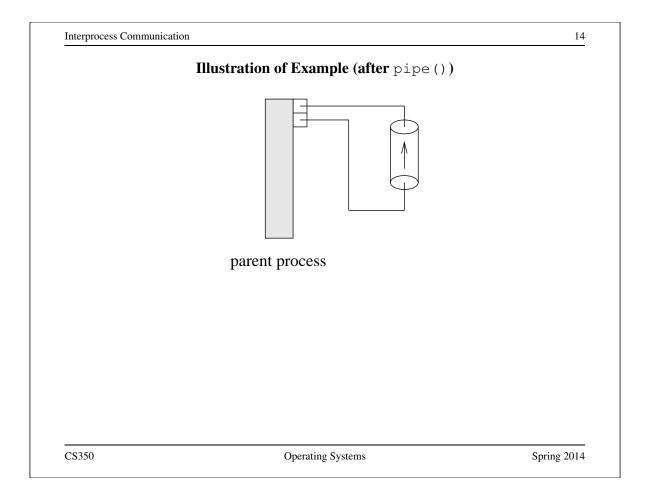




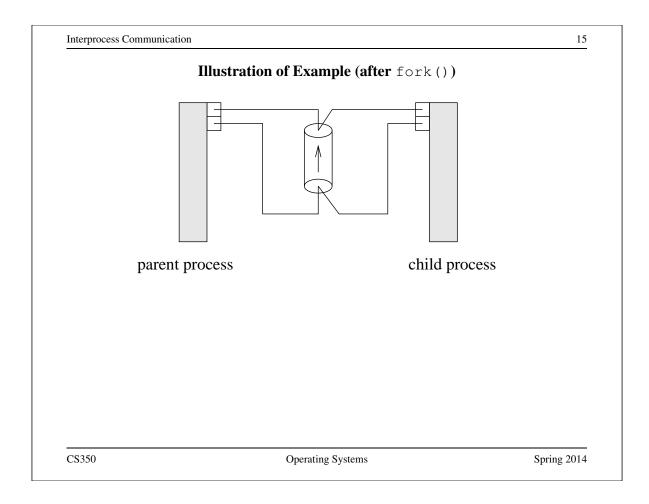
Interprocess Communication

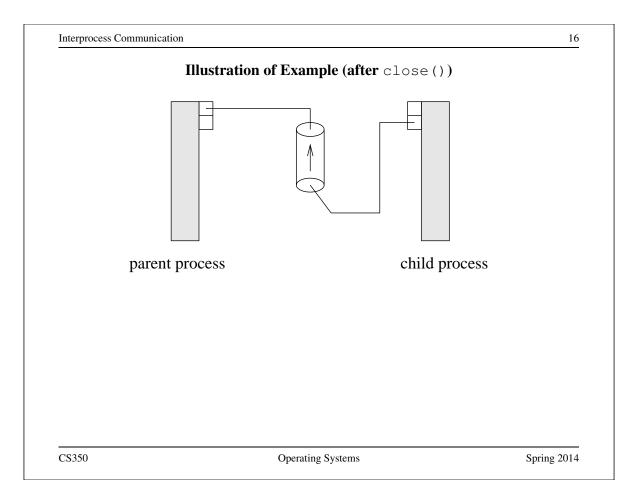
One-way Child/Parent Communication Using a Simplex Pipe





Spring 2014





Implementing IPC

- application processes use descriptors (identifiers) provided by the kernel to refer to specific sockets and pipes, as well as files and other objects
- kernel *descriptor tables* (or other similar mechanism) are used to associate descriptors with kernel data structures that implement IPC objects
- kernel provides bounded buffer space for data that has been sent using an IPC mechanism, but that has not yet been received
 - for IPC objects, like pipes, buffering is usually on a per object basis
 - IPC end points, like sockets, buffering is associated with each endpoint

