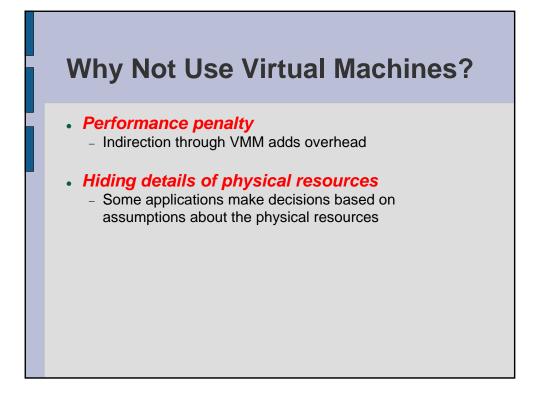
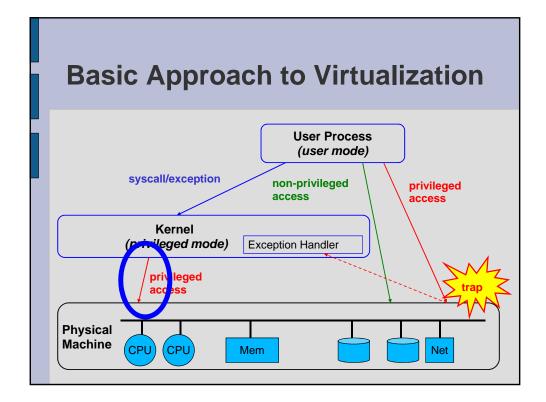
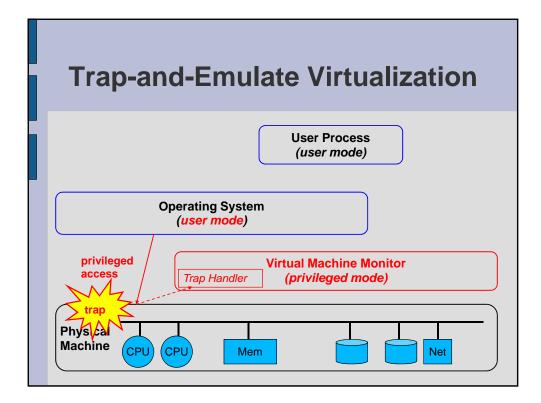


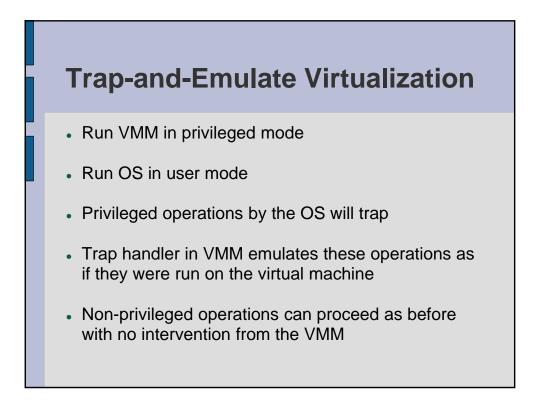
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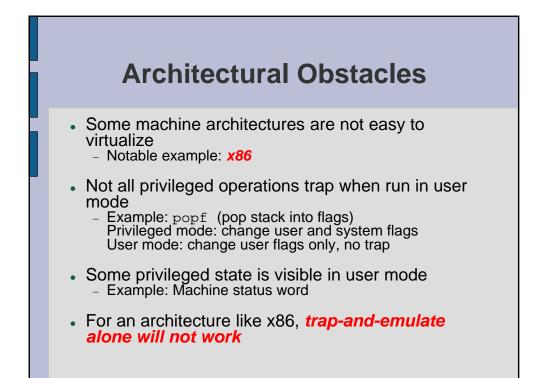
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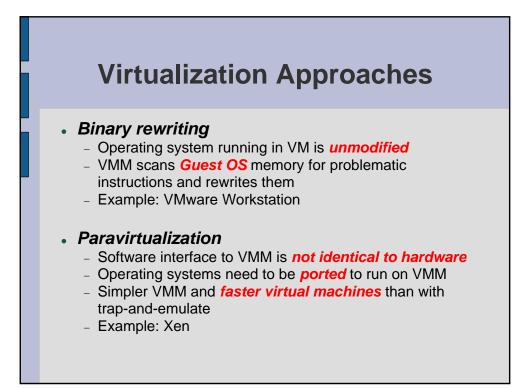












## Hardware Virtualization for x86

- Intel and AMD have both introduced processor extensions to help virtualization (Intel VT, AMD-V)
- Processor is aware of multiple virtual machine contexts (like process control blocks, but for entire operating system)
- New instructions to start/resume a VM
- New *privilege level* for VMM
- VMM selects which events should *trap* (vmexit)
  - Manipulating interrupt state, interacting with TLB, accessing control registers, ...

