Threads and Concurrency 1 **Review: Program Execution** • Registers - program counter, stack pointer, ... • Memory - program code - program data - program stack containing procedure activation records • CPU - fetches and executes instructions Winter 2015

CS350	
00000	

Operating Systems

	What is a Thread?
A threa	d represents the control state of an executing program.
A threa	d has an associated context (or state), which consists of
- the p the s (priv	processor's CPU state, including the values of the program counter (PC stack pointer, other registers, and the execution mode vileged/non-privileged)
– a sta	ck, which is located in the address space of the thread's process
Imagine	that you would like to suspend the program execution, and resume
it again need in suspend	later. Think of the thread context as the information you would order to restart program execution from where it left off when it was ed.

CS350









Example: Creating Threads Using thread_fork ()

```
for (index = 0; index < NumMice; index++) {
  error = thread_fork("mouse_simulation thread",
    NULL, mouse_simulation, NULL, index);
  if (error) {
    panic("mouse_simulation: thread_fork failed: %s\n",
    strerror(error));
    }
}
/* wait for all of the cats and mice to finish */
for(i=0;i<(NumCats+NumMice);i++) {
    P(CatMouseWait);
}</pre>
```

What kern/synchprobs/catmouse.c actually does is slightly more elaborate than this.

CS350

Operating Systems

Winter 2015

```
Threads and Concurrency
                                                                   8
       Example: Concurrent Mouse Simulation Threads (simplified)
static void mouse_simulation(void * unusedpointer,
                               unsigned long mousenumber)
  int i; unsigned int bowl;
  for(i=0;i<NumLoops;i++) {</pre>
    /* for now, this mouse chooses a random bowl from
     * which to eat, and it is not synchronized with
     * other cats and mice
     */
    /* legal bowl numbers range from 1 to NumBowls */
    bowl = ((unsigned int)random() % NumBowls) + 1;
    mouse_eat(bowl);
  }
  /* indicate that this mouse is finished */
 V(CatMouseWait);
  /* implicit thread_exit() on return from this function */
}
                                                             Winter 2015
CS350
                            Operating Systems
```

Threads	and Concurrency	
	Context Switch, Scheduling, and Dispatching	
• th ar	e act of pausing the execution of one thread and resuming the execution of nother is called a <i>(thread) context switch</i>	
• W	hat happens during a context switch?	
1	. decide which thread will run next	
2	. save the context of the currently running thread	
3	. restore the context of the thread that is to run next	
• th th	e act of saving the context of the current thread and installing the context of e next thread to run is called <i>dispatching</i> (the next thread)	f
• so	ounds simple, but	
-	- architecture-specific implementation	
-	- thread must save/restore its context carefully, since thread execution continuously changes the context	
-	- can be tricky to understand (at what point does a thread actually stop? wh is it executing when it resumes?)	at
CS350	Operating Systems Winter 2	201:

scheduling means deciding which thread should run next scheduling is implemented by a <i>scheduler</i> , which is part of the thread libra
scheduling means deciding which thread should run next scheduling is implemented by a <i>scheduler</i> , which is part of the thread libra
scheduling is implemented by a scheduler, which is part of the thread libra
simple <i>round robin</i> scheduling:
- scheduler maintains a queue of threads, often called the <i>ready queue</i>
- the first thread in the ready queue is the running thread
- on a context switch the running thread is moved to the end of the ready
queue, and new first thread is allowed to run
 newly created threads are placed at the end of the ready queue
more on scheduling later



CS350

Operating Systems

Threads and Concurrency 12 Preemption • without preemption, a running thread could potentially run forever, without yielding, blocking, or exiting • to ensure *fair* access to the CPU for all threads, the thread library may preempt a running thread • to implement preemption, the thread library must have a means of "getting control" (causing thread library code to be executed) even though the running thread has not called a thread library function • this is normally accomplished using *interrupts*

	Review: Interrupts	
• an	interrupt is an event that occurs during the execution of a progra	am
• int con	errupts are caused by system devices (hardware), e.g., a timer, a ntroller, a network interface	disk
• wh	en an interrupt occurs, the hardware automatically transfers con ation in memory	trol to a fixed
• at t <i>int</i>	that memory location, the thread library places a procedure calle errupt handler	ed an
• the	interrupt handler normally:	
1.	saves the current thread context (in OS/161, this is saved in a <i>tr</i> the current thread's stack)	<i>cap frame</i> on
2.	determines which device caused the interrupt and performs dev processing	vice-specific
3.	restores the saved thread context and resumes execution in that where it left off at the time of the interrupt.	context
CS350	Operating Systems	Winter 2015

<u>Treads and Concurrency</u>
<u>IPreemptive Round-Robin Scheduling</u>
In preemptive round-robin scheduling, the thread library imposes a limit on the amount of time that a thread can run before being preempted
the amount of time that a thread is allocated is called the scheduling *quantum*when the running thread's quantum expires, it is preempted and moved to the back of the ready queue. The thread at the front of the ready queue is dispatched and allowed to run.
the quantum is an *upper bound* on the amount of time that a thread can run once it has been dispatched
the dispatched thread may run for less than the scheduling quantum if it yields, exits, or blocks before its quantum expires









The OS/161 thread Structure

```
/* see kern/include/thread.h */
struct thread {
char *t_name;
                           /* Name of this thread */
const char *t_wchan_name; /* Wait channel name, if sleeping */
threadstate_t t_state; /* State this thread is in */
/* Thread subsystem internal fields. */
struct thread_machdep t_machdep; /* Any machine-dependent goo */
struct threadlistnode t_listnode; /* run/sleep/zombie lists */
                                /* Kernel-level stack */
void *t_stack;
struct switchframe *t_context; /* Register context (on stack) */
struct cpu *t_cpu;
                               /* CPU thread runs on */
struct proc *t_proc;
                               /* Process thread belongs to */
 . . .
CS350
                           Operating Systems
                                                         Winter 2015
```

Threads and Concurrency 20 **Review: MIPS Register Usage** R0, zero = ## zero (always returns 0) = ## reserved for use by assembler R1, at R2, = ## return value / system call number v0 R3, v1 = ## return value R4, a0 = ## 1st argument (to subroutine) R5, = ## 2nd argument al R6, a2 = ## 3rd argument R7, аЗ = ## 4th argument CS350 Winter 2015 **Operating Systems**

Review: MIPS Register Usage

R08-R15,	t0-t7	=	##	temps (not preserved by subroutines)
R24-R25,	t8-t9	=	##	temps (not preserved by subroutines)
			##	can be used without saving
R16-R23,	s0-s7	=	##	preserved by subroutines
			##	save before using,
			##	restore before return
R26-27,	k0-k1	=	##	reserved for interrupt handler
R28,	gp	=	##	global pointer
			##	(for easy access to some variables)
R29,	sp	=	##	stack pointer
R30,	s8/fp	=	##	9th subroutine reg / frame pointer
R31,	ra	=	##	return addr (used by jal)

CS350

Operating Systems

Winter 2015

```
Threads and Concurrency
                                                                  22
                   Dispatching on the MIPS (1 of 2)
/* See kern/arch/mips/thread/switch.S */
switchframe_switch:
  /* a0: address of switchframe pointer of old thread. */
  /* al: address of switchframe pointer of new thread. */
   /* Allocate stack space for saving 10 registers. 10*4 = 40 */
   addi sp, sp, -40
        ra, 36(sp)
                    /* Save the registers */
   SW
        gp, 32(sp)
   SW
   SW
        s8, 28(sp)
        s6, 24(sp)
   SW
        s5, 20(sp)
   SW
        s4, 16(sp)
   SW
        s3, 12(sp)
   SW
   SW
        s2, 8(sp)
        s1, 4(sp)
   SW
   SW
        s0, 0(sp)
   /* Store the old stack pointer in the old thread */
        sp, 0(a0)
   SW
CS350
                            Operating Systems
                                                             Winter 2015
```

Dispatching on the MIPS (2 of 2)

```
/* Get the new stack pointer from the new thread */
   lw
        sp, 0(a1)
                  /* delay slot for load */
   nop
   /* Now, restore the registers */
        s0, 0(sp)
   lw
   lw
        s1, 4(sp)
   lw
        s2, 8(sp)
   lw
        s3, 12(sp)
   lw
      s4, 16(sp)
        s5, 20(sp)
   lw
        s6, 24(sp)
   lw
   lw
        s8, 28(sp)
   lw
        gp, 32(sp)
   lw
        ra, 36(sp)
                         /* delay slot for load */
   nop
   /* and return. */
   j ra
   addi sp, sp, 40
                         /* in delay slot */
   .end switchframe_switch
CS350
                            Operating Systems
```

<u>Dispatching on the MIPS (Notes)</u>
Not all of the registers are saved during a context switch
This is because the context switch code is reached via a call to thread_switch and by convention on the MIPS not all of the registers are required to be preserved across subroutine calls
thus, after a call to switchframe_switch returns, the caller (thread_switch) does not expect all registers to have the same values as they had before the call - to save time, those registers are not preserved by the switch
if the caller wants to reuse those registers it must save and restore them

Winter 2015